## CAT System Computer Control

## Overview

The CAT (Computer Aided Transceiver) System in the MARK-V FT-1000MP provides control of frequency, VFO, memory, and other settings such as dualchannel memories and diversity reception using an external personal computer. This allows multiple control operations to be fully automated as single mouse clicks or keystroke operations on the computer keyboard.

The MARK-V FT-1000MP has a built-in level converter, allowing direct connection from the rear-panel CNT jack to the serial port of your computer without the need of any external boxes.

Each time a command instruction is being received from the computer via the CNT port, the "CNT" indicator appears in the display, then turns off afterward. You will need a serial cable for connection to the RS232C (serial or COM port) connector on your computer. Purchase a standard serial cable (not the so-called "null modem" type), ensuring it has the correct gender and number of pins (some serial COM port connectors use a 9-pin rather than 25-pin configuration). If your computer uses a custom connector, you may have to construct the cable. In this case, refer to the technical documentation supplied with your computer for correct data connection.

Vertex Standard does not produce CNT System operating software due to the wide variety of personal computers and operating systems in use today. However, the information provided in this chapter explains the serial data structure and opcodes used by the CMT system. This information, along with the short programming examples, is intended to help you start writing programs on your own. As you become more familiar with CNT operation, you can customize programs later on for your operating needs and discover the true operating potential of this system.

## CAT Data Protocol

Serial data is passed via the $\mathbf{X N T}$ jack on the rear panel of the transceiver at 4800 bits $/ \mathrm{sec}$. All commands sent from the computer to the transceiver consist of five-byte blocks, with up to 200 ms between each byte. The last byte sent in each block is the instruction opcode, while the first four bytes of each block are arguments: either parameters for that instruction, or dummy values (required to pad the block out to five bytes):

| CAT 5-BYTE COMMAND STRUCTURE |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Byte 1 | Byte 2 | Byte 3 | Byte 4 | Byte 5 |
| Argument | Argument | Argument | Argument | Instruction <br> OPCODE |

Each byte sent consists of one start bit, 8 data bits, no parity bit and two stop bits:

| CAT DATA BYTE FORMAT |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Start <br> Bit | 0 | B | 1 | 1 | 1 | 1 | 1 | 1 | Stop <br> Bit | Stop <br> Bit |

There are twenty-nine instruction opcodes for the MARK-V FT-1000MP, listed in the table on pages 94 ~ 97. Most of these duplicate menu programming settings or options, or else emulate front panel button functions. Notice that several instructions require no specific parameters. However, every Command Block sent to the transceiver must always consist of five bytes.

The $\mathbf{N T}$ control program you are writing must construct the 5-byte block, by selecting the appropriate instruction opcode, organizing the parameters, if any, and providing unused (dummy) argument bytes for padding the block to its required 5-byte length (the dummy bytes can contain any value). The resulting five bytes are then sent, opcode last, from the computer to the MARK-V FT-1000MP CPU via the serial port and CAT jack on the transceiver rear panel.

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## Constructing and Sending CAT Commands

Example \#1: Set Main VFO-A to 14.25000 MHz ;
$\square$ First determine the opcode for the desired instruction by referring to the CXT Commands Table. A good idea would be to store these opcodes within the program, so they can be looked up when the user requests the corresponding command.

- Here the instruction is "Set Main VFO Frequency," so the opcode (last byte of the block) is $\mathbf{0 A H}$.
Note- "H" s following each byte value indicate hexadecimal (base 16) values.
$\square$ Build the four argument byte values from the desired frequency by breaking it into 2-digit blocks (BCD "packed decimal" format). Note that a leading zero is always required in the hundreds-of-MHz place (and another in the tens-of- MHz if below 10 MHz ).
$\square$ Breaking 14.250.00 MHz into its BCD component, we arrive at:

| $\begin{gathered} 10 ’ \mathrm{~s} \\ \mathrm{~Hz} \end{gathered}$ | $\begin{gathered} \hline 100 \text { 's } \\ \mathrm{Hz} \end{gathered}$ | $\begin{gathered} \hline \hline \text { 1's } \\ \mathrm{kHz} \end{gathered}$ | $\begin{aligned} & \hline \hline \text { 10's } \\ & \text { kHz } \end{aligned}$ | $\begin{aligned} & \hline 100 \text { 's } \\ & \text { kHz } \end{aligned}$ | $\begin{gathered} \hline \text { 1's } \\ \mathrm{MHz} \end{gathered}$ | $\begin{aligned} & \hline \hline 10 \text { 's } \\ & \text { MHz } \end{aligned}$ | $\begin{aligned} & \hline 100 ’ s \\ & \mathrm{MHz} \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 5 | 2 | 4 | 1 | 0 |
| 00 |  | 50 |  | 42 |  | 01 |  |
| Byte 1 |  | Byte 2 |  | Byte 3 |  | Byte 4 |  |

$\square$ Inserting the 4-byte BCD-coded frequency (00, 50, 42, 01), the resulting 5-byte block should now look like this (again, in hex format):

| Byte 1 | Byte 2 | Byte 3 | Byte 4 | Byte 5 |
| :---: | :---: | :---: | :---: | :---: |
| 00 | 50 | 42 | 01 | OAH |
| DATA/ARGUMENT BYTES |  |  |  | OPCODE |

$\square$ Send these five bytes to the transceiver, in the order shown in the table above - from left-to-right: $\mathbf{0 0}$ 504201 0AH.

Example \#2: Activate a RX Clarifier Offset of +3.5 kHz .
$\square$ Clarifier settings are controlled from opcode 09H. The first four parameter bytes determine the type of offset, direction, and frequency displacement.
$\square$ According to the example, the first byte would be $50(500 \mathrm{~Hz})$, the second $03(3000 \mathrm{~Hz})$, followed by $\mathbf{0 0 H}$ (for +offset), 81H (TX CLAR on) and then opcode 09 H . Remember that the 1st and 2nd bytes are in BCD format.
$\square$ Completing the command byte sequence, we would send, in sequence, $50 \mathrm{H}, 03 \mathrm{H}, 00 \mathrm{H}, 81 \mathrm{H}, 09 \mathrm{H}$, to effect the Tx Clarifier offset.

| Byte 1 | Byte 2 | Byte 3 | Byte 4 | Byte 5 |
| :---: | :---: | :---: | :---: | :---: |
| 50 H | $\mathbf{0 3 H}$ | 00 H | $\mathbf{8 1 H}$ | $\mathbf{0 9 H}$ |
| DATA/ARGUMENT BYTES |  |  |  | OPCODE |

You should be getting a feel for the CMT command structuring sequence, let's move to the next step; reading transceiver operational data.

## CAT System Computer Control

## Downloading MARK-V FT-1000MP Data

On command, the MARK-V FT-1000MP will download some or all ( 1,863 bytes) of its operational data. This data block contains all current transceiver settings. In addition, the current meter indication ( Tx or Rx ) is read, digitized and returned as well. This provides a wealth of information in near real-time that can be processed by your program or the running application for control purposes or display readouts. By regular or intermittent requests for this data, the program (and you) can be kept continuously up-to-date on the status of the MARK-V FT-1000MP operating environment.

The following four commands cause the MARK-V FT-1000MP to download various operational and internally stored settings via the CNT port:

Status Update (10H) - causes the transceiver to return all or portions of its RAM table (up to 1,863 bytes).

Status Flags Request (FAH) - obtains only the first 6 bytes (the Status Flags), plus 2 extra "Model ID" bytes ( 10 H and 00 H ).

Read Meter (F7H) - returns the meter deflection (0 - FFH) repeated in four bytes, followed by one "filler" byte (F7H).

Pacing Command (0EH) - Each byte of returned data may be delayed by an interval determined by this command ( 0 to 255 ms in 1 -ms steps). This delay is initially zero until the Pacing command is sent (see note below).

Note: Pacing allows returned data to be read and processed by slower computers. However, set it as short as your computer will allow, to minimize the inconvenience of the delay. Sending all 1,863 bytes requires just under 5 seconds with zerolength delay selected, but over 5 minutes if the maximum delay is selected!

| Parameter | Bytes Returned | Data Returned | Comment |
| :---: | :---: | :---: | :---: |
| $\mathrm{U}=00 \mathrm{H}$ | 1,863 | All Status Updata Data | See above Box <br> - Pacing Command |
| $\mathrm{U}=01 \mathrm{H}$ | 1 | Memory <br> Channel No | Current or Last Selected Memory |
| $\mathrm{U}=02 \mathrm{H}$ | 16 | Current Operating Data (VFO or Memory) | See the Tables on page 91 and 92 for |
| $\mathrm{U}=03 \mathrm{H}$ | $\begin{gathered} 32 \\ (2 \times 16) \end{gathered}$ | Main VFO-A \& Sub VFO-B Data | 16-byte data record structures |
| $\mathrm{U}=04 \mathrm{H}^{*}$ | 16 | Memory Data |  |
| $\begin{gathered} X= \\ 00 \sim 71 \mathrm{H} \end{gathered}$ | NA | $\begin{aligned} & X=\text { Memory (1~99, P1~P5, Q1~Q5) } \\ & \circledast \text { only used when U }=04 \mathrm{H} \end{aligned}$ |  |

## Status Update Data Organization

An overview of the Status Update Data that can be returned to the PC in response to one of the Status Update requests (opcodes $\mathbf{1 0 H}, \mathbf{F A H}, \mathbf{F 7 H}$, or OEH) is shown next page. The 1,863 -byte block begins with six bytes, each containing one-bit state Status Flags (A), for a total of 48 bits, followed by one byte indicating the current (or last selected) Memory Channel (B), followed by $116 \times 16$-byte data records: one for the current Operating Data (C), one each for VFO-A (D) and VFO-B (E), and one for each of the 113 memories (F).

Of the four commands that cause Status Update to be returned, remember that only opcode 10 H (with its last argument set to zero) returns all of the data (see bottom left of this page).

## STATUS FLAGS (BYTES 1~ 6)

Each of the first six bytes are subdivided into 1 -bit flag fields: if a bit is set (1), the function is enabled (on); and if reset ( 0 ), the function is disabled (off). These flags reflect the current states of various transceiver functions, most of which appear in the radio display as indicators or LEDs. The Status Flags command returns these bytes for use in the control program (you could replicate these indicators on the computer display, or else use them as control flags for routines, etc.). Bit offsets for all six bytes is shown on page 89 .

## MEMORY CHANNEL DATA (BYTE 7)

The seventh Update Data Byte contains a binary value from $\mathbf{0 0} \sim \mathbf{7 0 H}$, corresponding to the current memory channel number on the display. Only this byte is returned by sending the Status Update command with the first parameter set to 1 . The chart on page 90 lists the corresponding hexadecimal codes for memory channels $01 \sim 99$, P1 ~ P9, and QMB memories $1 \sim 5$.

## 16-BYTE DATA RECORDS (BYTES 8 ~ 1863)

The remainder of the operational data returned by the Status Update command consist of 16-byte data records, indicating VFO and memory-specific selections. The first of these records is for the current display, followed by the VFO-A, VFO-B, and then the 113 memory channels, from lowest to highest. Please review the table at the top left column on page 91, which outlines the structure of a 16 -byte data record. Each byte is identified by its offset from the start (base address) of the record. A further breakdown of each byte offset is also provided.

Note that this same 16-byte data record format is used for the VFO and Memory Data as well, unless you are currently operating on a retuned memory ("M TUNE" displayed).

## CAT System Computer Control

## Status Update Data Organization

| 1863-Byte Status Updata Data (sent L-to-R) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Status <br> Flags | Memory Channel No. | Operating Data | VFO-A Data | VFO-B Data | Memory Data |
| 6 byte | 1 byte | 16 byte | 16 byte | 16 byte | 16 bytes (x 113 memories = 1808 bytes total) |
| (A) | (B) | (C) | (D) | (E) | (F) |

## 6-Byte Status Flags Record Table

| Bit <br> Offset | STATUS FLAG BYTE \#1 CONTENTS |
| :---: | :--- |
| 0 | Split Frequency Operation |
| 1 | Dual Receive Operation |
| 2 | Antenna Tuning In Progress |
| 3 | CXT System Activated |
| 4 | SUB VFO-B In-Use (Rx/Tx LED on) |
| 5 | Keypad Entry In Progress |
| 6 | Main Receiver Muted |
| 7 | PTT Keyed (Tx Active) |


| Bit <br> Offset | STATUS FLAG BYTE \#4 CONTENTS |
| :---: | :--- |
| 0 | 2nd IF 455 kHz Filter Selection Active |
| 1 | 1st IF 8.2 MHz Filter Selection Active |
| 2 | N/A |
| 3 | N/A |
| 4 | PTT Keyed via ©NT Command |
| 5 | General Coverage TX Inhibit |
| 6 | Key Release Timer Active |
| 7 | Tx Inhibit |


| Bit <br> Offset | STATUS FLAG BYTE \#2 CONTENTS |
| :---: | :--- |
| 0 | 5-sec. MEM CHK Timer Active |
| 1 | Memory Checking In Progress |
| 2 | Dual VFO Tracking Active |
| 3 | Quick Memory Bank Selected |
| 4 | Memory Tuning Active |
| 5 | VFO Operation |
| 6 | Memory Operation |
| 7 | General Coverage Reception |


| Bit <br> Offset | STATUS FLAG BYTE \#5 CONTENTS |
| :---: | :--- |
| 0 | RTTY TX Idle |
| 1 | N/A |
| 2 | N/A |
| 3 | Grouped Memory Mode Active |
| 4 | ANT B Selected |
| 5 | RX ANT Selected |
| 6 | PMS Tuning Active |
| 7 | AM Synchronous Mode Active |


| Bit <br> Offset | STATUS FLAG BYTE \#3 CONTENTS |
| :---: | :--- |
| 0 | FAST Tuning Active |
| 1 | Antenna Tuner (ATU) In-Line |
| 2 | SUB VFO-B Locked |
| 3 | MAIN VFO-A Locked |
| 4 | Squelch Closed |
| 5 | Scan Direction (Up/Down) |
| 6 | Scan Paused |
| 7 | Auto Memory Write Scanning Active |


| Bit <br> Offset | STATUS FLAG BYTE \#6 CONTENTS |
| :---: | :--- |
| 0 | Sub Receiver Audio Muted |
| 1 | Main Receiver Audio Muted |
| 2 | Dual VFO Tracking |
| 3 | N/A |
| 4 | N/A |
| 5 | VFO Channel Stepping |
| 6 | Tuner Wait (while tuning) |
| 7 | AM Synchronous Mode Active |

## CAT System Computer Control

## Selecting Update Data to Download

As mentioned before, there are four opcodes that cause the MARK-V FT-1000MP to report (update) its operating status by downloading all or a portion of its 1,863 data bytes. These opcodes are shaded in the CATT Commands table (pages 94 ~ 97).

Status Update (Opcode 10H) - The 1st and 4th parameters of this command allow selecting different portions of Status data to be returned, as follows (" X " is the 1st parameter, " U " is the 4th):

Read Flags (Opcode FAH) -This command can be set to retrieve all six Status Flag bytes, or else five bytes - three Status Flag Bytes, plus two transceiver ID bytes. The Status Flag Bytes are described on the preceding page, and in the Record Tables on the previous page.

The transceiver ID bytes are used in programs to distinguish the MARK-V FT-1000MP from other models, which have different, unique values returned in this situation. The constant values of 03 H and 93 H are returned by the MARK-V FT-1000MP (and only the MARK-V FT-1000MP), as shown:

| Flag Byte | Flag Byte | Flag Byte | ID Byte 1 | ID Byte 2 |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 2 | 3 | $(03 \mathrm{H})$ | $(93 \mathrm{H})$ |

Read Meter Data (Opcode F7H) - Sending this command returns a digitized meter deflection indication, between $\mathbf{0 0}$ and $\mathbf{F F H}$ (usually around $\mathbf{F O H}$ maximum). Four copies of this value are returned, along with one padding byte (F7H), as follows:

| Meter <br> Byte | Meter <br> Byte | Meter <br> Byte | Meter <br> Byte | F7H |
| :---: | :---: | :---: | :---: | :---: |

During reception, the signal strength deflection is returned. During transmission, the parameter represented by the reading returned depends on the setting of the METER switch.

## 1-Byte Memory Channel Number Data Structure

This identifies the current or last-selected memory channel $1 \sim 99$, P1 ~ P5 or QMB $1 \sim 5$ for operation. The table below translates hexadecimal codes into corresponding memory channel numbers. Please read the note in the box at the page bottom.

| Memory Channel Data (Hex Codes) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Ch. | Hex | Ch. | Hex | Ch. | Hex | Ch. | Hex |
| 01 | 00 H | 31 | 1 EH | 61 | 3 CH | 91 | 5 AH |
| 02 | 01 H | 32 | 1 FH | 62 | 3 DH | 92 | 5 BH |
| 03 | 02 H | 33 | 20 H | 63 | 3 EH | 93 | 5 CH |
| 04 | 03 H | 34 | 21 H | 64 | 3 FH | 94 | 5 DH |
| 05 | 04 H | 35 | 22 H | 65 | 40 H | 95 | 5 EH |
| 06 | 05 H | 36 | 23 H | 66 | 41 H | 96 | 5 FH |
| 07 | 06 H | 37 | 24 H | 67 | 42 H | 97 | 60 H |
| 08 | 07 H | 38 | 25 H | 68 | 43 H | 98 | 61 H |
| 09 | 08 H | 39 | 26 H | 69 | 44 H | 99 | 62 H |
| 10 | 09 H | 40 | 27 H | 70 | 45 H | P1 | 63 H |
| 11 | 0 AH | 41 | 28 H | 71 | 46 H | P2 | 64 H |
| 12 | 0 BH | 42 | 29 H | 72 | 47 H | P3 | 65 H |
| 13 | 0 CH | 43 | 2 AH | 73 | 48 H | P4 | 66 H |
| 14 | 0 DH | 44 | 2 BH | 74 | 49 H | P5 | 67 H |
| 15 | 0 EH | 45 | 2 CH | 75 | 4 AH | P6 | 68 H |
| 16 | 0 FH | 46 | 2 DH | 76 | 4 BH | P7 | 69 H |
| 17 | 10 H | 47 | 2 EH | 77 | 4 CH | P8 | 6 AH |
| 18 | 11 H | 48 | 2 FH | 78 | 4 DH | P9 | 6 BH |
| 19 | 12 H | 49 | 30 H | 79 | 4 EH | Q1 | 6 CH |
| 20 | 13 H | 50 | 31 H | 80 | 4 FH | Q2 | 6 DH |
| 21 | 14 H | 51 | 32 H | 81 | 50 H | Q3 | 6 EH |
| 22 | 15 H | 52 | 33 H | 82 | 51 H | Q4 | 6 FH |
| 23 | 16 H | 53 | 34 H | 83 | 52 H | Q5 | 70 H |
| 24 | 17 H | 54 | 35 H | 84 | 53 H |  |  |
| 25 | 18 H | 55 | 36 H | 85 | 54 H |  |  |
| 26 | 19 H | 56 | 37 H | 86 | 55 H |  |  |
| 27 | 1 AH | 57 | 38 H | 87 | 56 H |  |  |
| 28 | 1 BH | 58 | 39 H | 88 | 57 H |  |  |
| 29 | 1 CH | 59 | 3 AH | 89 | 58 H |  |  |
| 30 | 1 DH | 60 | 3 BH | 90 | 59 H |  |  |
|  |  |  |  |  |  |  |  |

## Important Note!

The Hex Memory Channel Codes for returned memory data shown above (Byte 7) are different than those used in upload command data (opcodes)!

The memory channel hex codes used as argument (parameter) bytes for opcodes are offset by one (that is, one value greater) from their returned data counterparts. Therefore the channel hex codes used in opcodes $02 \mathrm{H}, 03 \mathrm{H}$, and 0 DH would range from $01 \mathrm{H} \sim 71 \mathrm{H}$.
When constructing command block bytes, ensure that the correct memory channel hex code is used!

## 16-Byte Data Record Structure

The following tables outline the 16-byte data record structure common to the Operating Data, VFO-A, VFO$B$ and Memory Data records. The table below shows assignments for each of the 16-bytes in the Operating Data Record.

| Byte | 16-Byte Data Record Assignment |
| :---: | :--- |
| 0 | Band Selection |
| 1 |  |
| 2 | Operating Frequency |
| 3 |  |
| 4 |  |
| 5 | Clarifier Offset |
| 6 |  |
| 7 | Operating Mode |
| 8 | IF Filter Offset |
| 9 | VFO/MEM Operating Flags |
| A ~ F | Not Used |

Band Selection - The $0.1 \sim 30 \mathrm{MHz}$ transceiver operating range is divided into 28 bands, represented in hexadecimal format in the table below. Data read in this record after downloading is in binary format, and must be converted to hexadecimal, then translated to the corresponding band.

| Hex <br> Code | Band | Hex <br> Code | Band |
| :---: | :---: | :---: | :---: |
| 01 H | $0.1 \sim 0.5 \mathrm{MHz}$ | 0 FH | $10.5 \sim 12.0 \mathrm{MHz}$ |
| 02 H | $0.5 \sim 1.5 \mathrm{MHz}$ | 10 H | $12.0 \sim 14.0 \mathrm{MHz}$ |
| 03 H | $1.5 \sim 1.8 \mathrm{MHz}$ | 11 H | $14.0 \sim 14.5 \mathrm{MHz}$ |
| 04 H | $1.8 \sim 2.0 \mathrm{MHz}$ | 12 H | $14.5 \sim 15.0 \mathrm{MHz}$ |
| 05 H | $2.0 \sim 2.5 \mathrm{MHz}$ | 13 H | $15.0 \sim 18.0 \mathrm{MHz}$ |
| 06 H | $2.5 \sim 3.0 \mathrm{MHz}$ | 14 H | $18.0 \sim 18.5 \mathrm{MHz}$ |
| 07 H | $3.0 \sim 3.5 \mathrm{MHz}$ | 15 H | $18.5 \sim 21.0 \mathrm{MHz}$ |
| 08 H | $3.5 \sim 4.0 \mathrm{MHz}$ | 16 H | $21.0 \sim 21.5 \mathrm{MHz}$ |
| 09 H | $4.0 \sim 6.5 \mathrm{MHz}$ | 17 H | $21.5 \sim 22.0 \mathrm{MHz}$ |
| 0 AH | $6.5 \sim 7.0 \mathrm{MHz}$ | 18 H | $22.0 \sim 24.5 \mathrm{MHz}$ |
| 0 BH | $7.0 \sim 7.5 \mathrm{MHz}$ | 19 H | $24.5 \sim 25.0 \mathrm{MHz}$ |
| 0 CH | $7.5 \sim 8.0 \mathrm{MHz}$ | 1 AH | $25.0 \sim 28.0 \mathrm{MHz}$ |
| 0 DH | $8.0 \sim 10.0 \mathrm{MHz}$ | 1 BH | $28.0 \sim 29.0 \mathrm{MHz}$ |
| 0 OHH | $10.0 \sim 10.5 \mathrm{MHz}$ | 1 CH | $29.0 \sim 30.0 \mathrm{MHz}$ |

The Band Selection data byte is divided into two 4bit fields, representing the first and second value of the band number hex code. The Bit 0 and Bit 1 of the first field are used as flags for the memory mask and scan skip feature. A bit value of "1" means enabled, and " 0 " for disabled. Each value of the hex code is entered into its respective field in 4-bit binary format. The table below outlines the Data Byte field, and show an example of how the $24.5 \sim 25.0 \mathrm{MHz}$ band would be read as:

| Band Selection Data Byte (0) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit 0* | Bit 1** | Bit 2 | Bit 3 | Bit 4 | Bit 5 | Bit 6 | Bit 7 |
| Field 1 |  |  |  | Field 2 |  |  |  |
| 0* | 0** | 0 | 1 | 1 | 0 | 0 | 1 |
| *Mem Mask | $\begin{gathered} * * S c a n \\ \text { Skip } \end{gathered}$ | $0001=1$ |  | $1001=9$ |  |  |  |
| $\begin{aligned} & " 0 "=\text { Off } \\ & " 1 "=\text { On } \end{aligned}$ |  | $\begin{gathered} 19 \mathrm{H}=24.5 \sim 25.0 \mathrm{MHz} \\ \text { (refer to band chart) } \\ \hline \hline \end{gathered}$ |  |  |  |  |  |

Operating Frequency - Likewise, the current operating frequency is similarly coded, this time into four bytes comprised of eight fields, from MSB to LSB. For example, a read binary value of 0000000000010101 1011111001101000 is 15BC68 $($ HEX $)=14.250 .00$ MHz as follows:

| Operating Frequency Data Bytes (1-4) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Byte 1 |  | Byte 2 |  | Byte 3 |  | Byte 4 |  |
| Field 1 MSB | Field 2 | Field 3 | Field 4 | Field 5 | Field 6 | Field 7 | $\begin{gathered} \text { Field } 8 \\ \text { LSB } \end{gathered}$ |
| 00000 | 010000 | $00\|0\| 1$ | O11011 | ${ }_{1}\|0\| 111$ | $\left.{ }_{1}\right\|_{1} 110$ |  | 10000 |
| 0 | 0 | 1 | 5 | B | c | 6 | 8 |
| $0015 \mathrm{BC} 68(\mathrm{HEX})=1,425,000=14.250 .00 \mathrm{MHz}$ |  |  |  |  |  |  |  |

Clarifier Offset - Clarifier offset is written using 16bit binary data in two bytes. Negative offsets are expressed in binary $2 s$-complement format, with a leading bit flag value* of " 1 ." Although frequency resolution below 10 Hz cannot be viewed, absolute Clarifier offsets down to 0.625 Hz can be read from downloaded data.

Arithmetic conversion must be done on the binary value to arrive at the actual frequency offset (multiplying the 16-bit binary offset by 0.625 ). For example, a binary value of 0011111001101111 (3E6FH or 15,983) multiplied by 0.625 results in an offset of +9989.375 Hz .

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## 16-Byte Data Record Structure

A value of 1011111001101111 (the 2-s complement of the previous example) produces a minus offset of -9989.375 Hz . See the byte chart below for a breakdown of the conversion process.

| Clarifier Offset Data Bytes (5-6) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Byte 5 |  |  |  |  |  |  |  |  | Byte 6 |  |  |  |  |  |  |  |  |
| 1* | 0 | 1 | 1 | 1 | 1 |  | 1 | 0 | 0 | 0 | 1 | 1 | 0 |  |  | 1 | 1 |
| $\begin{gathered} 1^{*} \text { ("-" flag) } 011111001101111= \\ \text { (-) 3E6F }(\mathrm{HEX})=(-) 15,983 \\ (-) 15,983 \times 0.625=(-) 9989.375 \mathrm{~Hz} \end{gathered}$ |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |

*Note - Remember that the first bit is a flag: "0" for positive offsets, " 1 " for negative offsets, and is not included in calculations.

Operating Mode - The operating mode is expressed as a three-bit binary code in offsets $5 \sim 7$. Bit 0 contains a User Mode flag, while Bits 1~4 contain "dummy" values (unused).

| Operating Mode Byte (7) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit 0* | Bit 1 | Bit 2 | Bit 3 | Bit 4 | Bit 5 | Bit 6 | Bit 7 |
| User Mode | N/A - "dummy bytes |  |  |  | Mode Data (3-bit Code) |  |  |
| 0 | X | X | X | X | 0 | 1 | 0 |
| 0XXXX010 = CW operation, User Mode Off |  |  |  |  |  |  |  |
| $\begin{aligned} & 0=\text { off } \\ & 1=\text { on } \end{aligned}$ | Bits 1 ~ 3 are "dummy bits" any $1 / 0$ combination may appear in here, but is insignificant. |  |  |  | LSB 000 <br> USB 001 <br> CW 010 <br> AM 011 <br> FM 100 <br> RTTY 101 <br> PKT 110 |  |  |

IF Filter Selection - The first data bit (Bit 0) contains a flag indicating normal or alternate reception mode (see table). The remainder of the data byte contains $2 \times 4$-bit fields separated by a dummy bit. The first field holds the 3-bit binary code for the 8.2 MHz 2nd IF filter selection, while the second holds the 455 kHz 3rd IF selection. Codes are listed in the bottom of the table below.

| IF Filter Selection Byte (8) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit 0* | Bit 1 | Bit 2 | Bit 3 | Bit 4 | Bit 5 | Bit 6 | Bit 7 |
| RX Mode | 8.2 MHz 2nd IF |  |  | X | 455 kHz 3 rd IF |  |  |
|  | $\begin{aligned} & \text { Thru } \\ & 2.4 \mathrm{k} \\ & 2.0 \mathrm{k} \\ & 500 \\ & 250 \end{aligned}$ |  | 000 |  | $\begin{aligned} & 6.0 \mathrm{k} \\ & 2.4 \mathrm{k} \\ & 2.0 \mathrm{k} \\ & 500 \\ & 250 \end{aligned}$ |  | 000 |
|  |  |  | 001 |  |  |  | 001 |
|  |  |  | 010 |  |  |  | 010 |
|  |  |  | 011 |  |  |  | 011 |
|  |  |  | 100 |  |  |  | 100 |
| Mode* |  | CW | AM | RTTY |  |  | PKT |
| 0 |  | USB | ENV |  | LSB |  | LSB |
| 1 |  | LSB | SYN |  | USB |  | FM |

VFO/MEM Indicators - Five flags indicate the status of Clarifier (Rx \& Tx), Repeater Offset (+/-), and Antenna Selection (A/B/RX). Bits 0 and 1 are not used (dummy values).

| Clarifier, RPT, ANT Status Byte (9) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit 0* | Bit 1 | Bit 2 | Bit 3 | Bit 4 | Bit 5 | Bit 6 | Bit 7 |
| $\begin{gathered} \text { TX } \\ \text { CLAR } \end{gathered}$ | $\begin{gathered} \mathrm{RX} \\ \mathrm{CLAR} \end{gathered}$ | +RPT | -RPT |  |  | X | X |
| Note: for all flag bits, $1=\mathrm{On}, 0=\mathrm{Off}$ for ANT SELECT: $00=$ ANT-A, $01=$ ANT-B, $10=$ RX ANT |  |  |  |  |  |  |  |

## CAT System Computer Control

## Coding Examples

Although Vertex Standard does not provide CAT control software (owing to the large variety of computers and operating systems used by our customers), the following are a few examples of critical CAT I/O functions, in Basic. Note that all variations of Basic may not support some of the commands, in which case alternate algorithms may need to be developed to duplicate the functions of those shown.

## Sending a Command

After "opening" the computer's serial port for 4800baud, 8 data bits and 2 stop bits with no parity, as I/O device \#2, any CAT command may be sent. However, if you determine that your computer may need extra time to process data returned from the transceiver, you should send the Pacing command first. Here is an example of the Pacing command setting a $2-\mathrm{ms}$ delay:

## PRINT \#2, <br> CHR\$(0);CHR\$(0);CHR\$(0);CHR\$(2);CHR\$(\&HE);

Notice that the instruction opcode is sent last, with the first (MSB) parameter sent just before it, and the LSB parameter (or dummies) sent first. This means that the parameters are sent in the reverse order from that in which they appear in the CTT Commands table. Also note that in this and the following examples, we are sending zeros as dummy bytes; this is not necessary, however. If you decide to send commands through a 5-byte array, the values of the dummy parameters need not be cleared. Also note the semicolon at the end of the line, to prevent Basic from sending extra bytes to "end the line" (the ©NT system control system is based on binary streams, not text streams).

Using the same example as on page 87, the following command could be used to set the frequency of the display to 14.25000 MHz :

## PRINT \#2, <br> CHR\$(\&H00);CHR\$(\&H50);CHR\$(\&H42);CHR\$(\&H01); CHR\$(\&HA);

Notice here that the BCD values can be sent just by preceding the decimal digits with "\&H" in this ex-
ample. However, in an actual program, it may be preferable to convert the decimal frequency variable in the program to an ASCII string, and then to convert the string to characters through a lookup table.

If you send a parameter that is out of range for the intended function, or not among the specified legal values for that function, the MARK-V FT-1000MP should do nothing. Therefore, you may wish to alternate your sending regular commands or command groups with the Read Flags or short-form Update commands, allowing the transceiver to let the computer know if everything sent so far has been accepted and acted upon as expected.

Bear in mind that some commands specify "binary," as opposed to BCD formatted parameters. You can send binary parameters without going through the character/hex string conversion process. For example, the CH parameter in the Command table is a binary value. You could have the MARK-V FT-1000MP recall memory channel 50 (decimal) by the following:

PRINT\#2,
CHR\$(0);CHR\$(0);CHR\$(0);CHR\$(49);CHR\$(2);
Note that we must send 49 to get channel 50, since the channel numbers in the command start from 0 , while those on the display start with 1.

## Reading Returned Data

The reading process is easily done through a loop, storing incoming data into an array, which can then be processed after the entire array has been read. To read the meter:

## FOR I=1 TO 5

MDATA(I) = ASC(INPUT\$(1,\#2))
NEXT I
Recall from above that the meter data consists of four identical bytes, followed by a filler byte, so we really only need to see one byte to get all of the information this command offers. Nevertheless, we must read all five bytes (or 1, 16, or 1,863, in the case of the Update data). After reading all of the data, we can select the bytes of interest to us from the array (MDATA, in the above example).

## CAT System Computer Control

Opcode Command Chart (1)

| Command or Key | Parameter Bytes |  |  |  | Opcode | Parameter Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1st | 2rd | 3rd | 4th | 5th |  |
| SPLIT | - | - | - | T | 01H | Split Tx/Rx operation ON ( $\mathrm{T}=\mathbf{0 1 H}$ ) or OFF ( $\mathrm{T}=\mathbf{0 0 H}$ ) |
| Recall Memory | - | - | - | X | 02H | Recalls memory number $\mathrm{X}: \mathbf{0 1 H} \sim \mathbf{7 1 H}$, corresponding to memories $1 \sim 99$, P1 ~ P9, and QMB $1 \sim$ QMB 5. |
| VFO/MEM | - | - | K | X | 03H | Enter ( $\mathrm{K}=\mathbf{0 0 H}$ ), Mask ( $\mathrm{K}=\mathbf{0 1 H}$ ) or Un-Mask ( $\mathrm{K}=\mathbf{0 2 H}$ ), memory channel X ( $\mathbf{0 1 H} \sim \mathbf{7 1 H}$ ). |
| LOCK | - | - | - | P | 04H | Tuning knob Lock/Unlock: <br> $\mathrm{P}=\mathbf{0 0 H}$ : Main Dial Lock $\quad \mathrm{P}=\mathbf{0 1 H}$ : Main Dial Unlock <br> $\mathrm{P}=\mathbf{0 2 H}$ : Sub Dial Lock $\quad \mathrm{P}=\mathbf{0 3 H}$ : Main Dial Unlock |
| A/B | - | - | - | V | 05H | Select VFO-A $(\mathrm{V}=00 \mathrm{H})$, or $\mathrm{VFO}=\mathrm{B}(\mathrm{V}=02 \mathrm{H})$. |
| [M-B] | - | - | - | X | 06H | Copy memory X (01H |
| UP ( $\mathbf{A}$ ) | - | - | U | V | 07H | Step VFO-A/B (V = 00H/01H) up by $100 \mathrm{kHz} / 1 \mathrm{MHz}(\mathrm{U}=\mathbf{0 0 H} / \mathbf{0 1 H})$. |
| DOWN ( $\boldsymbol{\nabla}$ ) | - | - | D | V | 08H | Step VFO-A/B (V = 00H/01H) down by $100 \mathrm{kHz} / 1 \mathrm{MHz}(\mathrm{D}=\mathbf{0 0 H} / \mathbf{0 1 H})$. |
| CLAR | C1 | C2 | C3 | C4 | 09H | Clarifier offset direction \& frequency in BCD <br> $\mathrm{C} 1=\mathrm{Hz}$ offset $\quad(\mathrm{C} 1=\mathbf{0 0 H} \sim 99 \mathrm{H})$ <br> $\mathrm{C} 2=\mathrm{kHz}$ offset $\quad(\mathrm{C} 2=00 \mathrm{H} \sim 09 \mathrm{H})$ <br> $\mathrm{C} 3=\mathrm{Hz}$ offset $\quad(\mathrm{C} 3=00 \mathrm{H} / \mathrm{FFH})$ <br> Clarifier On/Off/Reset: <br> $\mathrm{C} 4=\mathrm{RX}$ CLAR ON/OFF (C4 $=\mathbf{0 0 H} / \mathbf{0 1 H}$ ) <br> TX CLAR ON/OFF (C4 = 80H/81H) <br> CLAR CLEAR $\quad(\mathrm{C} 4=\mathbf{F F H})$ |
| Set Main VFO-A Operating Freq. | F1 | F2 | F3 | F4 | OAH | New operating frequency in BCD format (F1 ~ F4) see text for formatting example. |
| MODE | - | - | - | M | OCH | Select Operating Mode M: <br> $\begin{array}{lll}\text { LSB: } M=00 \mathrm{H} & \text { USB: } M=01 \mathrm{H} & \text { CW: } M=02 \mathrm{H} \\ \text { CW (R): } M=03 \mathrm{H} & \text { AM: } M=04 \mathrm{H} & \text { AM(Sync): } M=05 \mathrm{H} \\ \text { FM: } M=06 \mathrm{H} & \text { FM-W: } M=07 \mathrm{H} & \text { RTTY (L): } M=08 \mathrm{H} \\ \text { RTTY (U): } M=09 H & \text { PKT (L): } M=0 A H & \text { PKT (F): } M=0 B H\end{array}$ |
| Pacing | - | - | - | N | OEH | Add N-millisecs ( $\mathbf{0 0 H} \sim \mathbf{F F H}$ ) delay between each byte of all downloaded data returned from the transceiver |
| PTT | - | - | - | T | 0FH | Transmitter ON ( $\mathrm{T}=\mathbf{0 1 H )}$ or OFF ( $\mathrm{T}=00 \mathrm{H}$ ) |
| Status Update | X | - | - | U | 10H | Instructs the radio to return 1,16,32, or 1863 bytes of Status Updata data. X is significant only when $\mathrm{U}=1 \sim 4$. <br> $\mathrm{X}=00 \mathrm{H} \sim 71 \mathrm{H}$ : desired memory channel ( 1 ~ 99, P1 ~ P9, or QMB 1 ~ QMB 5) <br> $\mathrm{U}=00 \mathrm{H} \quad$ All 1863 byte <br> $\mathrm{U}=01 \mathrm{H} \quad$ 1-byte Memory Channel Number <br> $\mathrm{U}=02 \mathrm{H} \quad$ 16-byte Operating Data <br> $\mathrm{U}=03 \mathrm{H} 2 \times 16$-byte VFO (A \& B) Data <br> $U=04 \mathrm{H} \quad 1 \times 16$-byte Memory Data |

## CAT System Computer Control

Opcode Command Chart (2)

| Command or Key | Parameter Bytes |  |  |  | Opcode | Parameter Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1st | 2rd | 3rd | 4th | 5th |  |
| Electronic Keyer | K1 | K2 | K3 | K4 | 70H | Activates remote control and contest keyer functions. $\mathrm{K} 1=\mathbf{0 0 H}(\text { fixed value })$ <br> K2 = keyer function: <br> $\mathbf{0 0 H}=$ Message 0 <br> $01 \mathrm{H}=$ Message 1 <br> 02H = Message 2 <br> 03H = Message 3 <br> 04H = CQ/ID Message <br> $05 \mathrm{H}=$ Contest Number <br> 06H = Decrement Contest Number <br> 07H = Increment Contest Number <br> 08H = Message Playback m/o Tx <br> $09 \mathrm{H}=$ Write Message into Memory <br> $\mathrm{K} 3=\mathbf{0 1 H}$ (fixed value) <br> K4 $=1 \mathrm{BH}$ (fixed value) |
| EDSP <br> Enhanced <br> Digital <br> Signal <br> Plocessing | - | - | P1 | P2 | 75H | EDSP Settings, where P 2 is: <br> RX EDSP OFF (30H), P1 = 00H <br> AM EDSP Demodulation On (31H), P1 = 00H <br> USB EDSP Demodulation (32H), <br> with audio response of $100 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=\mathbf{0 0 H})$ or <br> 300 Hz ~ $2.8 \mathrm{kHz}(\mathrm{P} 1=10 \mathrm{H})$ <br> LSB EDSP Demodulation (33H), <br> with audio response of $100 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=\mathbf{0 0 H})$ or <br> 300 Hz ~ $2.8 \mathrm{kHz}(\mathrm{P} 1=10 \mathrm{H})$ <br> AF Filter Off (40H), P1 = 00H <br> AF LPF On $(41 \mathrm{H})$, <br> where P1 = [Fcutoff (Hz)]/20 (HEX format) <br> AF HPF On (42H), <br> where P1 = [Fcutoff (Hz)]/20 (HEX format) <br> CW 240 Hz BWF (45H), <br> where P1 = FCENTER (BCD format) <br> CW 120 Hz BWF (46H), <br> where P1 = FCENTER (BCD format) <br> CW 60 Hz BWF (47H), <br> where P1 = Fcenter (BCD format) <br> Data Mode AF Filter On (48H), where P1 = FSK (10H), SSTV (20H), Packet (30H), or FAX (40H) <br> Random Noise Filter (4AH) Off/On ( $\mathrm{P} 1=\mathbf{0 0 H} / \mathbf{1 Y H}$ ) <br> Audio Notch Filter (4BH) Off/On (P1 = 00H/10H) <br> Noise Reducer (4EH), where P1 = Off ( 00 H ), <br> NR A (10H), NR B $\mathbf{( 2 0 H})$, NR C $(\mathbf{3 0 H})$, NR D $(\mathbf{4 0 H})$ <br> TX EDSP Off (BOH) <br> USB EDSP Modulation (B2H), with audio response of: $100 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=10 \mathrm{H}), 50 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=20 \mathrm{H})$, <br> $200 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=30 \mathrm{H}), 300 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=40 \mathrm{H})$ <br> LSB EDSP Modulation (B3H), with audio response of: <br> $100 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=10 \mathrm{H}), 150 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=20 \mathrm{H})$, <br> $200 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=30 \mathrm{H}), 300 \mathrm{~Hz} \sim 3.1 \mathrm{kHz}(\mathrm{P} 1=40 \mathrm{H})$, <br> TX Audio EDSP $(\mathbf{C 1 H})$, where P1 = Off $(\mathbf{O O H})$, <br> Bank 1 (10H), Bank $2(20 \mathrm{H})$, Bank 3 (30H), Bank 4 (40H) |

## CAT System Computer Control

Opcode Command Chart (3)

| Command or Key | Parameter Bytes |  |  |  | Opcode | Parameter Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1st | 2rd | 3rd | 4th | 5th |  |
| TUNER | - | - | - | T | 81H | Switch Antenna Tuner ON ( $\mathrm{T}=\mathbf{0 1 H}$ ) or OFF ( $\mathrm{T}=\mathbf{0 0 H}$ ) |
| Tuner Start | - | - | - | - | 82H | Start Antenna Tuning |
| Dual Operation | - | - | - | D | 83H | Switch Dual Receive ON ( $\mathrm{D}=01 \mathrm{H}$ ) or OFF ( $\mathrm{D}=00 \mathrm{H}$ ) |
| [RPT] | - | - | - | R | 84H | Switch Simplex Operation ( $R=\mathbf{0 0 H}$ ), Minus Shift $(R=01 H)$, or Plus Shift ( $\mathrm{R}=\mathbf{0 2 H}$ ) for Repeater Operation |
| [A -B ] | - | - | - | - | 85H | Copy Data Display in VFO-A or VFO-B. |
| Set SUB VFO-B <br> Operating Freq. | F1 | F2 | F3 | F4 | 8АH | Enter new operating frequency in F1 ~ F4, in BCD format: see text for example. |
| BANDWIDTH <br> 2nd \& 3rd IF Filter Selection | X1 | - | - | X4 | 8CH | Select filter bandwidth for selected IF (see below): |
| MEM. Channel Scan Skip | - | - | S | X | 8DH | Tag memory channels 1 through 99 ( $\mathrm{X}=\mathbf{0 1 H} \sim \mathbf{6 C H}$ ), to be skipped ( $\mathrm{S}=\mathbf{0 1 H}$ ) or included ( $\mathrm{S}=00 \mathrm{H}$ ) while scanning. |
| Step VFO-A UP/DOWN | - | - | - | T | 8EH | Step frequency of VFO-A UP ( $\mathrm{T}=\mathbf{0 0 H}$ ) or DOWN ( $\mathrm{T}=\mathbf{0 1 H}$ ) |
| CTCSS <br> Encoder Tone Frequency Select | E | E | E | E | 90 H | Select one of 33 CTCSS subaudible tones where $\mathrm{E}=\mathbf{0 0 H} \sim \mathbf{2 0 H}$ |
| Read Meter \& Panel Controls | - | - | - | - | F7H | Instruct radio to return digitized indications of various meter level readings and front panel control settings (4 repeated bytes, and F7H) selected by: <br> $\mathrm{M}=\mathbf{0 0 H}$ Main S-Meter $\mathrm{M}=\mathbf{8 7} \mathrm{H}$ TUN Meter <br> M = 01H Sub S-Meter $\quad M=$ FOH Shuttle Jog Dial <br> $\mathrm{M}=80 \mathrm{H}$ PO Meter <br> M = F1H CW Pitch Setting <br> $\mathrm{M}=\mathbf{8 1} \mathrm{H}$ ALC Meter <br> M $=\mathbf{F} 2 \mathrm{H}$ Remote Control A/D Level <br> $\mathrm{M}=83 \mathrm{H}$ IC Meter <br> $\mathrm{M}=\mathrm{F} 3 \mathrm{H}$ SHIFT Setting <br> $M=84 \mathrm{H}$ VCC Meter <br> $M=F 4 H$ WIDTH Setting <br> $M=85 \mathrm{H}$ SWR Meter $\quad M=$ F5H EDSP Contour Selection <br> $M=86 \mathrm{H}$ MIC Meter $\quad M=$ F6H EDSP NR Selection |
| Repeater Offset | X1 | X2 | X3 | X4 | F9H | Set offset for RPT shift, valid values are $0 \sim 500 \mathrm{kHz}$ in $1-\mathrm{kHz}$ step. Use BCD format for X2 ~ X4. <br> X1 is 10's \& 100's of Hz X2 is 1's \& 10's of kHz <br> X 3 is must be $00 \mathrm{H}, 01 \mathrm{H}$, or $02 \mathrm{H} \quad \mathrm{X} 4$ is must be 00 H |

## CAT System Computer Control

## Opcode Command Chart (4)

| Command or Key | Parameter Bytes |  |  |  | Opcode | Parameter Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1st | 2 d | 3rd | 4th | 5th |  |
| Read Internal Status Flags | - | - | - | F | FAH | Instructs radio to return either five or six status flag bytes. <br> 5-Byte Format (F = 00H) <br> Status Flag Byte \#1 <br> Status Flag Byte \#2 <br> Status Flag Byte \#3 <br> *ID Byte \#1 (03H) <br> *ID Byte \#2 (93H) <br> 6-Byte Format ( $\mathrm{F}=\mathbf{0 1 H}$ ) <br> Status Flag Byte \#1 <br> Status Flag Byte \#2 <br> Status Flag Byte \#3 <br> Status Flag Byte \#4 <br> Status Flag Byte \#5 <br> Status Flag Byte \#6 <br> * See page 89 for explanation of transceiver ID byte values. |

